

Parallel Puzzle-Solving

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Parallel Puzzle-Solving

- Unplugged activity that can be used in any course
- Simple version can be used to introduce shared-memory concepts
- More complex versions can be used for distributed-memory concepts
- Data-visualization can be used to explain Amdahl's & Gustafson's Laws

Setup: Children's Puzzles (from Thrift Stores)



Puzzles' sizes differ by factors of 10

Shared-Memory Parallel Exercise

1. Divide students into several “processor groups”
 - Single core, dual-core, quad-core, etc.
 - Number of groups and their sizes vary, depending on course enrollment
2. Give each group a puzzle
3. Repeat:
 - a. *Ready-set-go!* Start all groups and a timer simultaneously.
 - b. Time how long it takes each group to solve their puzzle (e.g., [online-stopwatch.com](https://www.online-stopwatch.com)); record that time in a spreadsheet
 - c. Disassemble puzzles, rotate them among the groups, reset the timer

Until each group has solved each puzzle

4. Lead discussion of students’ experiences & observations



Unicore
processor



Dual-core
processor

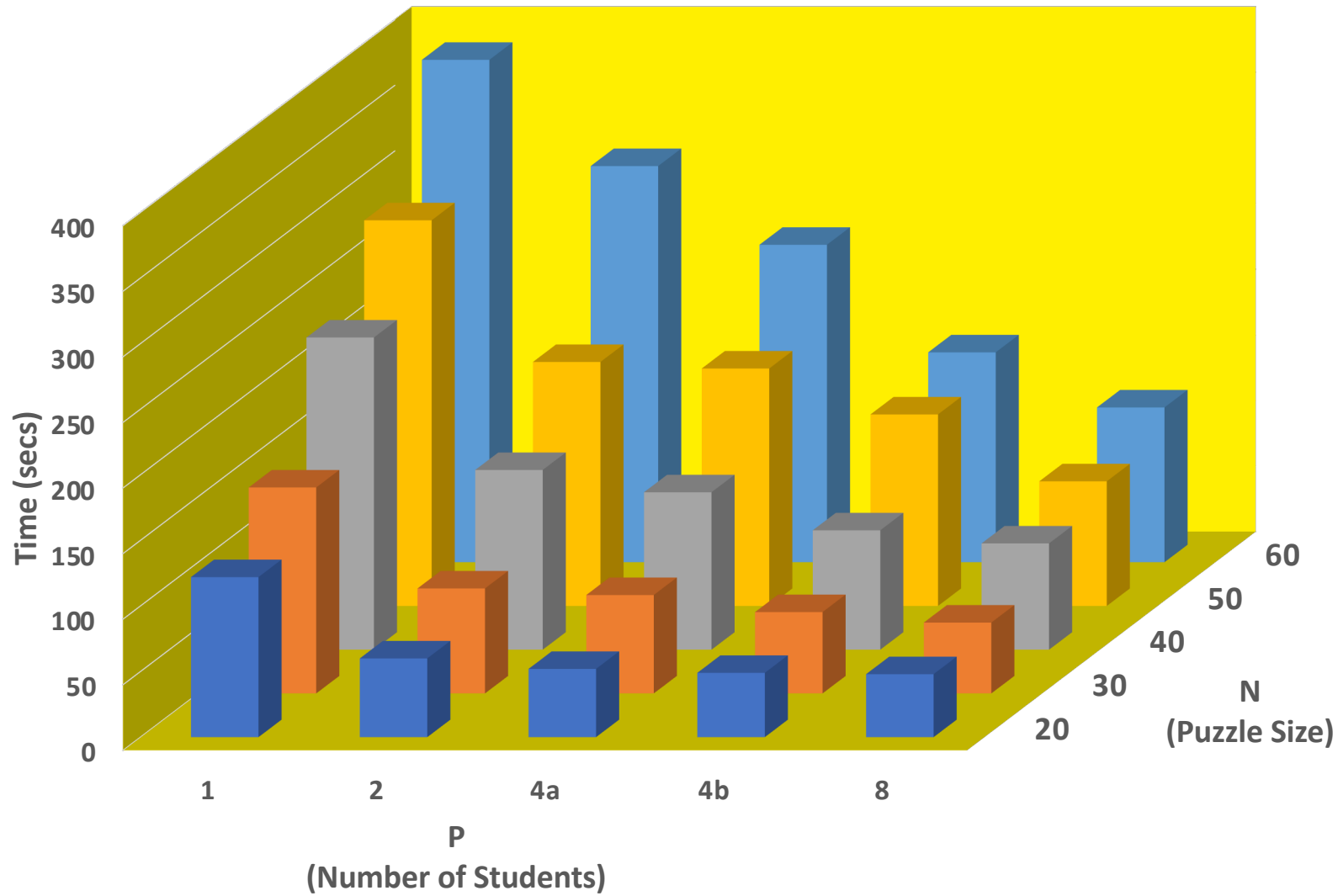


Quad-core
processor

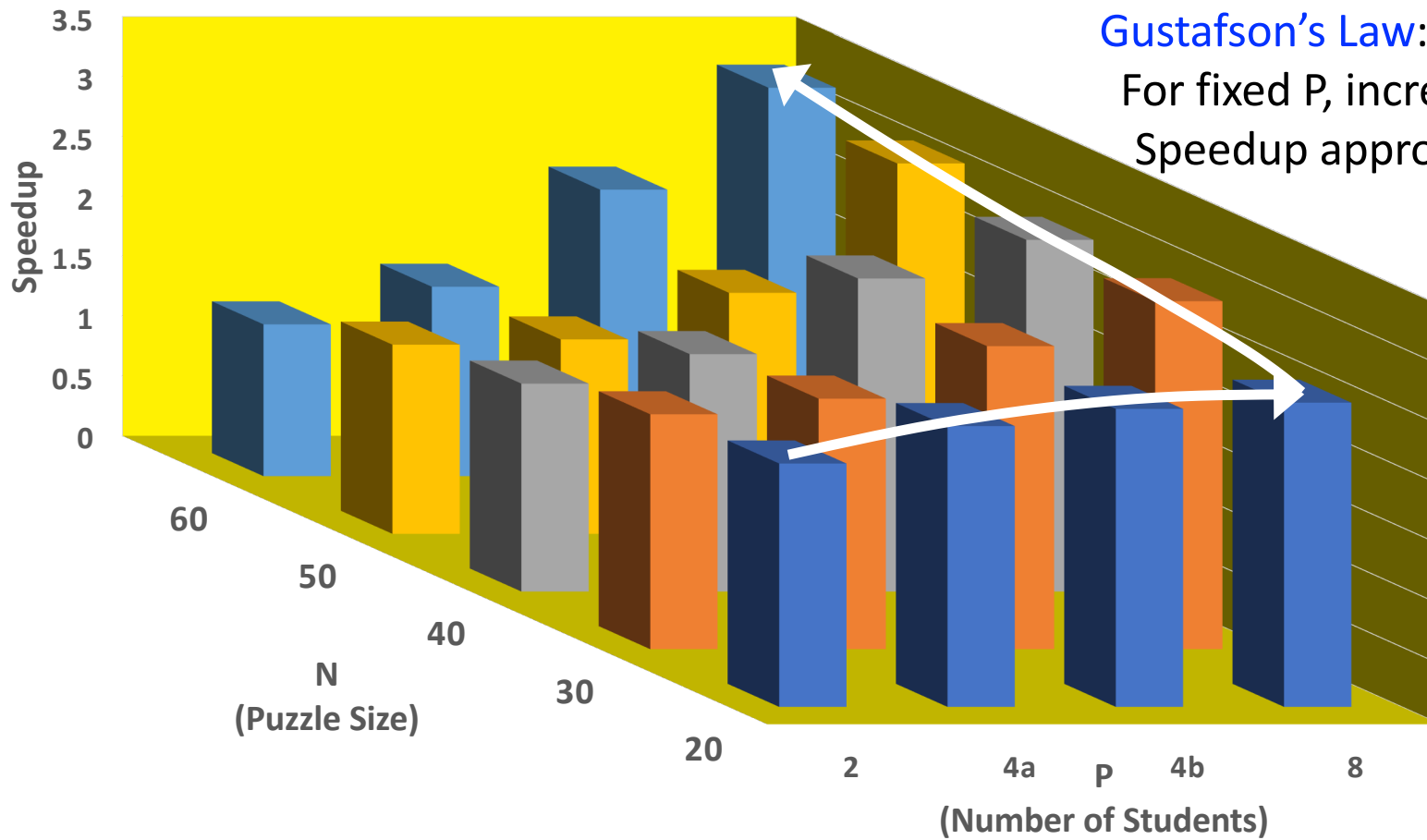


Octa-core
processor
(minus two
students so
we can see
the puzzle)

Times for Different-Sized Groups to Finish Different-Sized Puzzles



Speedup for Different-Sized Groups Finishing Different-Sized Puzzles



Gustafson's Law:

For fixed P, increasing N,
Speedup approaches P

Amdahl's Law:

For fixed N,
increasing P,
speedup
approaches
asymptote of
 $1/\text{sequentialPartTime}$

Parallel “Laws”

Definition: $Speedup_p = Time_1 / Time_p$

- **Amdahl’s Law**: Let $Time_1 = 1$. For a problem of size N and increasing P :

$$Speedup_p(N) = \frac{1}{\frac{parallelPartTime}{P} + sequentialPartTime}$$

$$\text{As } P \rightarrow \infty, Speedup_p(N) \rightarrow \frac{1}{sequentialPartTime}$$

- **Gustafson’s Law**: As N increases:

$$Speedup_p(N) = P + sequentialPartTime * (1 - P)$$

If $sequentialPartTime \rightarrow 0$ as $N \rightarrow \infty$, then $Speedup_p \rightarrow P$